

Curriculum Vitae

Dr. Klaus Havelund

Ph.D in Computer Science

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Born the 17th of October 1955 in Copenhagen, Denmark

The SRS nomination is given to individuals that have demonstrated achievement comparable to that required for an appointment as a professor at a leading university. See: <http://scienceandtechnology.jpl.nasa.gov/community/srscouncil>.

Education

1991–94 Ph.D in Computer Science from the University of Copenhagen,

Prepared at **Ecole Normale Supérieure, Paris.**

“The Fork Calculus - Towards A Logic for Concurrent ML”.

Development of a process algebra in the CCS family with the objective to study a specification language for the Concurrent ML (CML) programming language (details p.2).

1986 Master Thesis in Computer Science from the University of Copenhagen.

“Stepwise Development of a Denotational Stack Semantics”.

Study of the relationship between abstract and operational descriptions of programming languages (details p.3).

Professional Experience

July 2006 – present NASA's Jet Propulsion Laboratory, Pasadena, Los Angeles, USA.

From February 2009: *Senior Research Scientist*. From July 2007 - January 2009: *principal* computer scientist. From July 2006 - June 2007: contracting through Columbus Technologies. (details p.3).

Mar 2005 – July 2006 Kestrel Technology, California, USA (details p.4).

Apr 1997 – February 2005 Researcher at NASA Ames Research Center, California, USA. Contracting through Recom Technologies 1997-2001 and through Kestrel Technology 2001-2005 (details p.4).

Sep 1996–Mar 1997 Researcher at the Department of Computer Science, Aalborg University, Denmark (details p.5).

Oct 1994–Jul 1996 Researcher on a HCM (Human Capital Mobility) grant financed by the European Community. Research Lab: LITP, Paris 6, France (details p.5).

Jan–Oct 1994 Post-Doc at Ecole Polytechnique, Paris, France (details p.5).

1988–91 Researcher at CRI (advanced Danish software company) within the RAISE formal methods project, which in 1988 was transferred from DDC, see below (details p.5).

1984–88 Researcher at the industrial research institute DDC (Danish Datamatics Center) and member of the European ESPRIT project RAISE, the purpose of which was to develop a formal specification language (details p.6).

1979–82 Software programmer in various companies (half time during my university studies).

1 University Studies

1991–94 Ph.D in Computer Science from DIKU, Denmark

The Ph.D work was carried out from February 1991 to December 1993, and was defended March 1994 at DIKU, University of Copenhagen, Denmark. During this period I spent more than two years at Ecole Normale Supérieure, Paris, France.

The supervisor was Klaus Grue (DIKU). The members of the jury were Klaus Grue, Kim Guldstrand Larsen (AUC, Denmark) and Mogens Nielsen (DAIMI, Denmark).

The main motivation for writing a Ph.D was to extend the work I had carried out during 6 years in the European ESPRIT project RAISE.

The thesis, written in English, has as title: *“The Fork Calculus—Towards a Logic for Concurrent ML”*. The Fork Calculus, FC, presents a theory for dynamic process creation where processes interact through hand-shake communication. This calculus differs from Robin Milner’s CCS in the way that processes are put in parallel. In CCS there exists a binary parallel operator $|$ with which two processes p and q may be put in parallel as $p|q$. In FC this binary operator has been replaced with a unary `fork`-operator, and a process p can be activated to execute in the background, in parallel with the remaining program, with the command `fork(p)`. Also, FC has sequential composition instead of the action prefixing of CCS.

After having defined the syntax for FC, I have constructed an operational semantics, and based on that, I have studied various bisimulations, including a complete axiomatization of one of these. Two extensions of this calculus are then studied, one of which deals with program refinement, and one of which deals with dynamic process configuration as found in Robin Milner’s π -calculus. For each of these three calculi I have defined a Hennessy-Milner like modal logic.

The three calculi shall be seen as approximations to defining a refinement logic for the programming language CML (Concurrent ML). CML is an extension of the programming

language ML with concurrency primitives, amongst them a `fork`-operator. The thesis ends with an outline of such a logic for CML.

Part of the work has been carried out in collaboration with Kim Guldstrand Larsen, Aalborg University (AUC), Denmark. Hence, K. Larsen is co-author on published papers. The references from my thesis work are the following: [17, 59, 85]

1986 Master Thesis in Computer Science from DIKU, Denmark

The topic of the thesis [84] was denotational semantics of programming languages with professor Neil D. Jones as supervisor. I got the note : 9 out of 10 – corresponding to 11 on the Danish scale. The goal of the project was to bridge the gap between an abstract semantic definition of an Algol-like programming language, and a concrete operational definition of the same language. The bridge was created by a series of four intermediate semantics, getting more and more concrete. In particular the work revealed systematically the distinction between static (compile time) and dynamic (runtime) semantics.

During my studies I also wrote a syntax checker for the Meta-IV language, the specification language of the formal method VDM. This together with the above denotational semantics project was my real introduction to the area of formal methods that I have then stayed within for more than a decade now.

In general, the education at DIKU is planned as a five year study, with three years broad introduction to fundamental areas of computer science, followed by two years of more advanced topics, including the master thesis.

2 Professional Experience

July 2006 – present Jet Propulsion Laboratory, Los Angeles, USA

I am a *Senior Research Scientist* in the LaRS group (Laboratory for Reliable Software), employed by California Institute of Technology (Caltech). This position corresponds to Caltech tenure. From July 2006 - July 2007 I was contracting for JPL through Columbus Technologies and from 2007-2009 as a principal (Caltech employee). The group's purpose is to support JPL missions in producing reliable software. The group produces tool support for static as well as dynamic analysis.

Current projects:

- **RULER**: in search for a satisfactory runtime verification logic – convenient to use, expressive, covering state machines as well as logic, efficient to monitor, and easy to implement.
- *Survivable Software*: A GCC-based framework for static analysis, program instrumentation, program monitoring and recovery (currently for the C programming language).
- **MCAI 2.0**: Model Checking and Abstract Interpretation. Also referred to as : Computational Modeling and Analysis for Complex Systems. NSF Expeditions project.

My focus is inference of specifications from execution traces.
See: <http://cmacs.cs.cmu.edu>.

- *Java Coding Standard*: Development of a JPL institutional coding standard for Java. A set of coding rules that Java programmers should follow.

Past projects at JPL:

- LOGSCOPE: a tool for checking log files against temporal logic specifications. Implemented in Python.
- ARMOR: a CIL-based tool for instrumenting and monitoring C programs (the programming language used at JPL for flight software).
- LSCCS: Launch Site Command and Control System. A Python application for commanding and monitoring the space shuttle launch platform at Kennedy Space Center. The purpose was to explore the advantages and disadvantages of a domain specific language versus an extension of Python for this task.
- MAP: a translator from the ASPEN planning language (<http://aspen.jpl.nasa.gov>) to Promela, the modeling language of the SPIN model checker. The purpose was to use SPIN to verify plan-models.

Mar 2005– July 2006 Researcher at Kestrel Technology, California, USA

Worked on two NASA projects: Model-Centric Safety-Critical Java for Exploration (MXJ), the PI was Kestrel Institute, and Reliable Software Systems Development (RSSD), the PI was JPL (Gerard Holzmann). My main activity was the development of a specification language as an extension of Java for MXJ, and development of a C runtime monitoring system for RSSD. I also worked on a static analysis specification language.

1997–2005 Researcher at NASA Ames Research Center, California, USA

My activities concentrated on program verification and testing, and the goal was to develop techniques for locating errors in parallel programs.

I performed one of the more successful applications of model checking: the analysis of the Remote Agent for the Deep-Space 1 space craft, where we found errors that although corrected, later were re-introduced in other parts of the software, causing deadlock in flight. I furthermore engaged in projects involved with verification of planning systems, including the organization of VVPS'05, the 1st International Workshop on Verification and Validation of Model-Based Planning and Scheduling Systems, Monterey, June 2005.

I conceptualized and started the the Java PathFinder project, which thanks to colleagues today is the major project in the Automated Software Engineering group and has won two awards. Although I am not developer of the current system I started the project, and made the first prototype, which translated a substantial subset of Java into Promela, the modeling language of the SPIN model checker. This is pioneering work that has motivated other researchers on the international scene.

I worked on runtime verification where program executions are monitored and checked against requirement specifications. I started a series of runtime verification workshops, as documented on <http://www.runtime-verification.org>.

Sep 1996–Mar 1997 Researcher at Aalborg University, Denmark

I here worked in Kim Guldstrand Larsen’s formal methods group. This group is known for their expertise in model checking, and in particular real-time model checking. During this stay, I applied the real-time model checker UPPAAL (developed partly by this group) to a 10 year old real-life audio/video protocol from the Audio/Video company Bang & Olufsen. During this effort I spotted the source of a known error, which had been around throughout all those years without being identifiable by normal testing.

Oct 1994–Jul 1996 HCM Grant, Paris 6 University, France

I was here financed by a HCM (Human Capital Mobility) grant from the European Community to do research in concurrency verification. My work in Therese Hardin’s group was focused on formal specification and verification of concurrent real-time systems, for example communication protocols. Special emphasis was put on combining theorem proving in classical typed higher order logic with theorem proving in temporal logic. This was done basically by embedding TLA (‘Temporal Logic of Actions’ developed by Leslie Lamport) into the general purpose theorem prover PVS (‘Prototype Verification System’) developed by Owre, Shankar and Rushby at SRI International, California. A branch of this work consisted of combining theorem proving and model checking.

I collaborated with the people at SRI, in particular with Natarajan Shankar. A result of this collaboration is the paper [62] presented at the *Formal Methods Europe* conference at Oxford in March 1996. I have spent more than 5 months at SRI over the last 18 months.

As an additional result of this collaboration, I initiated a visit to Paris 6 by John Rushby, where he gave a one day PVS course for academics and industrial people. About 70 persons attended. My own contribution was to provide a “hands-on” practical exercise in using PVS on computers after the course of John Rushby. As a result of my stay at Paris 6, PVS is now used there in research (including a Ph.D student) and will soon be used in teaching. Also, the PVS system is now available via ftp from Paris 6, which then has become one of 3 European internet sites providing PVS in addition to SRI in California.

Jan–Oct 94 Post-Doc at Ecole Polytechnique, Paris

I worked as a post-doc in Radhia Cousot’s group, financed by Ecole Polytechnique. During this period I learned about the theorem prover PVS, and considered how it could be used to specify and verify parallel systems.

1988–91 Researcher at CRI — the RAISE Project, Denmark

In 1988 the Danish software house CRI took over parts of the activities of DDC, amongst these the European ESPRIT project RAISE, see below. Hence, as participant of this

project, and at that time employed by DDC, I was transferred from DDC to CRI. In general CRI was involved in several European ESPRIT projects, and in the European Space Agency's programs. Hence, an inspiring international environment.

1984–1988 Researcher at DDC — The RAISE Project, Denmark

DDC was an industrially oriented research institution, with main activities within ESPRIT projects (European research program for information technology). The initiator and scientific chief of the institute was Professor Dines Bjørner – DTU (Technical University of Denmark), currently director of the United Nations University for software technology in Macau.

I was working for 6 years as scientific staff in ESPRIT project 315: RAISE. RAISE stands for 'Rigorous Approach to Industrial Software Engineering'. The purpose of the project was to produce a formally (mathematically) based method for producing software. The overall goal was to combine VDM ('Vienna Development Method' developed by Dines Bjørner and Cliff Jones) with CSP ('Communicating Sequential Processes' developed by Hoare). The major teams of the project were the Danish DDC team and the British STC (Standard Telephones and Cables) team. However, also ABB and ICL were involved in the project. The project covered approximately 100 man years, with the partition between Danish and British effort being around 50:50.

The project had a number of internationally known computer scientists associated as consultants, who continuously followed the project. These were Manfred Broy, Cliff Jones, Don Sannella and Andrzej Tarlecki.

My main responsibilities throughout the 6 years were language design and semantics of the resulting language. The language design was carried out by a group of 10 people, and was based on case studies generated by the true industrial partners (STC, ABB, ICL). After the language design, I wrote the final language semantics together with Robert Milne during a period of approximately 8 months. The last I did in the project was to write the majority (85%) of the textbook (published by Prentice-Hall) which explains the RAISE specification language. That is, I wrote *all* of the 250 page tutorial, and half of the 100 page reference manual. See [1]. This book is used today as a general introduction to formal methods at the Technical University of Denmark. Other RAISE references are [13, 9].

I have written several reports during the project, often in collaboration with other members of the team, and some of these were so-called deliverables to the European Commission.

The RAISE product (a specification language and associated tools) has been tested in the 5 year follow up ESPRIT project 5383: LaCoS, which involved several companies in several European countries: CRI, BNR Europe, SYPRO, Bull, MATRA Transport, INISEL Espacio, SSI, Technisystems and Lloyd's Register of Shipping.

See information about RAISE on the World Wide Web on the address: "<http://dream.dai.ed.ac.uk/raise>". I was not involved in LaCoS since I decided to do a Ph.D directly after the end of the RAISE project.

3 Awards

- The *JPL Mariner award* for the successful delivery of the LogScope tool for MSL (Mars Science Laboratory). LogScope checks output log files against a formal specification and reports violations. The tool was delivered to the FIT [testing] team to support flight software testing. July 2009.
- *Outstanding Technology Development Award* for Java PathFinder (JPF), Federal Laboratory Consortium (FLC, see: <http://www.federallabs.org>) Far West Region Awards. July 2009.
- *Royal Academy of Engineering Distinguished Visiting Fellowship* at the University of Manchester during December 2008-April 2009.
- *ACM Distinguished Paper Award* for the paper: “*Racer: Effective Race Detection Using AspectJ*”, Eric Bodden and Klaus Havelund. Presented at the International Symposium on Software Testing and Analysis (ISSTA’08), Seattle, WA, July 2008.
- *NASA’s Group Achievement Award* to the Launch Control System Proof-of-Concept team. For successful demonstration of KSC’s Launch Control System Proof-of-Concept Architecture for the Constellation Program’s Command, Control and Communication Project. Signed by NASA’s Administrator, Michael D. Griffin, Washington DC, “*this eighth day of May Two Thousand Eight*”.
- *Award for contribution to a NASA Tech Brief article* : “*Automated Testing using Symbolic Execution and Temporal Monitoring*”, that highlights a NASA Ames innovation. September 2006.
- NASA Office of Aerospace Technology *Turning Goals Into Reality (TGIR) Engineering Innovation Award* for the Java PathFinder (JPF). June 2003. See <http://ti.arc.nasa.gov/story.php?id=76>.
- *EASST award for best software science paper* presented at ETAPS’02: “*Synthesizing Monitors for Safety Properties*”, Klaus Havelund and Grigore Rosu. Presented at the International Conference on Tools and Algorithms for Construction and Analysis of Systems (TACAS’02), Grenoble, France, April 2002.

4 Invited Presentations

- ASM’03, International Workshop on Abstract State Machines, Italy, March 2003.
- Joint CAV/ISSTA Special Event on Specification, Verification, and Testing of Concurrent Software. Boston, USA, July 2004.
- Danish industry, arranged by CISS (Centre for Embedded Software Systems), Aalborg, Denmark, December 2004.
- CASSIS’05, Construction and Analysis of Safe, Secure and Interoperable Smart devices, Nice, France, March 2005.

- VVEIS'05, The 3rd International Workshop on Verification and Validation of Enterprise Information Systems, Miami, Florida, May 2005.
- 8th JPL-GSFC Quality Mission Software Workshop, Santa Barbara, California, May 2006.
- PADTAD'06 (Parallel and Distributed Systems: Testing and Debugging). Portland, Maine, USA, July 17 2006.
- ARTIST2 Summer School. Autrans (near Grenoble), France, September 8-12, 2008.
- Talk for 150 10-11 graders (Space : The Final Frontier) from schools around Manchester, UK. April 1, 2009.
- COMPASS'09 (Correctness, Modeling, and Performance of Aerospace Systems). York, UK, March 28 2009. Satellite workshop of ETAPS'09.
- 9th International Workshop on Runtime Verification. June 26-28, 2009, Grenoble, France. Satellite workshop of CAV'09.
- Software Reliability for Space Missions. July 20, 2009, Pasadena, California, USA. Satellite workshop of SMC-IT'09.
- FMA'09 (Formal Methods for Aerospace). Eindhoven, the Netherlands, November, 2009. Satellite workshop of FM'09.

5 Organized Meetings, Workshops and Conferences

I have organized or steered (as a steering committee member) the following events.

- I co-organized the 7th International SPIN workshop in year 2000 (SPIN'00) at Stanford University, California. The workshop lasted 3 days and was stand-alone (was not associated to a bigger conference).
- I co-organized the 1st International Workshop on Verification and Validation of Model-Based Planning and Scheduling Systems, Monterey, California, USA, June 2005: <http://planning.cis.strath.ac.uk/vvpsws>.
- I started together with Grigore Rosu a sequence of so far nine workshops (eight have occurred and one is planned) on Runtime Verification (RV – starting with RV'01 in 2001 and including RV'09 in 2009), as documented on <http://www.runtime-verification.org>. I belong to the steering committee. Of the eight workshops I co-organized and co-chaired the following four:
 1. RV'01 - held as a CAV'01 satellite event in Paris, France, July 2001.
 2. RV'02 - held as a CAV'02 satellite event in Copenhagen, Denmark, July 2002.
 3. RV'04 - held as an ETAPS'04 satellite event in Barcelona, Spain, April 2004.
 4. FATES/RV'06 - held as a FLoC'06 event in Seattle, USA, August 2006

The remaining workshops (RV'03 with CAV in Colorado, RV'05 with CAV in Edinburgh, RV'07 with AOSD in Vancouver, RV'08 with ETAPS in Budapest, and RV'09 with CAV in Grenoble) were or are organized by people that the RV steering committee appointed. I feel responsible for the regular top level management of this workshop series and all its events.

- I co-organized a Dagstuhl meeting on runtime verification, January 3-6, 2007. See <http://www.dagstuhl.de/07011>.
- I was programme chair of the 15th International SPIN Workshop on Model Checking of Software in Los Angeles, California (stand-alone event), August 10-12, 2008. Program co-chair. See <http://compilers.cs.ucla.edu/spin08>.
- I was (due to my role as SPIN 2008 program chair) steering committee member for the SPIN workshop for the period 2008-2009.
- I was in the organization committee for NFM 2009, the 1'st NASA Formal Methods Symposium. Mountain View (Silicon Valley), California, USA, April 6-8, 2009. See <http://ti.arc.nasa.gov/event/nfm09>.
- I co-organized the 2nd International Workshop on Verification and Validation of Model-Based Planning and Scheduling Systems, Thessaloniki, Greece, September 2009: <http://www-vvps09.imag.fr/>.
- I am general co-chair for the first conference on Runtime Verification, Malta, November 1-4, 2010: <http://rv2010.org>.
- I am co-organizing a Dagstuhl meeting on Runtime Verification, Diagnosis, Planning and Control for Autonomous Systems, November 7-12, 2010. See <http://www.dagstuhl.de/en/program/calendar/semhp/?semnr=10451>.

6 Program Committee Memberships

As a Program Committee (PC) member for a particular event one must review and select papers for presentation at that event (workshop, conference).

- FASE 2011, The 14t'h International Conference on Fundamental Approaches to Software Engineering. Saarbrucken, Germany, 26 March - 4 April, 2011. Co-located with the The European Joint Conferences on Theory and Practice of Software (ETAPS 2011).
- Workshop on Autonomous Systems 2011, Saarbruecken, Germany, March/April, 2011. Co-located with the The European Joint Conferences on Theory and Practice of Software (ETAPS 2011).
- ICTSS 2010, IFIP 22nd International Conference on Testing Software and Systems. Natal, Brazil, November 8-10, 2010.
- HVC 2010, The IBM Verification Conference 2010. Haifa, Israel, October 5-7, 2010.

- SSV 2010, Systems Software Verification. Vancouver, BC, Canada, October 4-6, 2010. Co-located with the 9th USENIX Symposium on Operating Systems Design and Implementation (OSDI 2010).
- SEFM 2010, 8th IEEE International Conference on Software Engineering and Formal Methods. Pisa, Italy, September 13-17, 2010.
- MoChArt 2010, Sixth Workshop on Model Checking and Artificial Intelligence. Atlanta, Georgia, USA, July 11-12, 2010. Co-located with the Twenty-Fourth AAAI Conference on Artificial Intelligence (AAAI 2010).
- PADTAD 2010, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging. Trento, Italy, July 12-13, 2010. Co-located with ISSSTA 2010.
- DRHE 2010, The Seventh IARP Workshop on Technical Challenges for Dependable Robots in Human Environments. Toulouse, France, June 16-17, 2010.
- MSVVEIS 2010, The 8th International Workshop on Modelling, Simulation, Verification and Validation of Enterprise Information Systems. Funchal, Madeira, Portugal, June 8-12, 2010.
- PLDI 2010, Conference on Programming Language Design and Implementation. External Review Committee (ERC) member. Toronto, Canada, June 5-10, 2010.
- NFM 2010, Second NASA Formal Methods Symposium. Washington D.C, USA, April 13 - 15, 2010.
- SATE 2009, Static Analysis Tool Exposition Workshop. Arlington, Virginia, USA, November 6, 2009.
- VVPS 2009, 2nd International Workshop on Verification and Validation of Planning and Scheduling Systems, Thessaloniki, Greece, September 2009.
- PADTAD 2009, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging. Chicago, Illinois, USA, July 19-20, 2009. Co-located with ISSSTA 2009.
- CAV 2009, The 21'st International Conference on Computer Aided verification. Grenoble, France, June 26-July 2, 2009.
- RV 2009, The 9th International Workshop on Runtime Verification. Grenoble, France, June 26-June 28, 2009.
- SPIN 2009, 16th International SPIN Workshop on Model Checking of Software. Grenoble, France, June 26-28, 2009, Co-located with CAV 2009.
- MSVVEIS 2009, The Seventh International Workshop on Modelling, Simulation, Verification and Validation of Enterprise Information Systems, Milano, Italy, 2009.
- NFM 2009, The 1'st NASA Formal Methods Symposium. Mountain View (Silicon Valley), California, USA, April 6-8, 2009.
- VMCAI 2009, The 10th Intl. Conference on Verification, Model Checking, and Abstract Interpretation, Savannah, GA, January 18-20, 2009.

- SEFM 2008, 6th IEEE International Conference on Software Engineering and Formal Methods. November 10-14, 2008, Cape Town, South Africa.
- HVC 2008, The IBM Verification Conference 2008, Haifa, Israel, October 28-30, 2008.
- ICSEA 2008, The Third International Conference on Software Engineering Advances Sliema, Malta, October 26-31, 2008.
- SLE 2008, The First International Conference on Software Language Engineering, Toulouse, France, September 29-30, 2008.
- MSVVEIS 2008, The Sixth International Workshop on Modelling, Simulation, Verification and Validation of Enterprise Information Systems, Barcelona, Spain, 2008.
- PADTAD 2008, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging, Seattle, Washington, USA, July 20-21, 2008.
- SAW 2008, ACM SIGPLAN Static Analysis Workshop (Co-located with PLDI 2008). Tucson, Arizona, June 12, 2008.
- AOSD 2008, Seventh International Conference on Aspect-Oriented Software Development. Brussels, Belgium, March 31 - April 4, 2008.
- CONFENIS 2007, the IFIP International Conference on Research and Practical Issues of Enterprise Information Systems. October 14-16, 2007, Beijing, China.
- The IBM Verification Conference 2007. October 23 - 25, 2007. Organized by IBM Research Lab in Haifa, Israel.
- SEFM 2007, 5th IEEE International Conference on Software Engineering and Formal Methods. September 10-14, 2007, London, UK.
- ICSEA 2007, the Second International Conference on Software Engineering Advances August 25-31, 2007, Cap Esterel, French Riviera, France.
- PADTAD 2007, Parallel and Distributed Systems: Testing and Debugging. In conjunction with the International Symposium on Software Testing and Analysis (ISSTA), July 09, 2007, London, England.
- MSVVEIS 2007, the 5th International Workshop on Modelling, Simulation, Verification and Validation of Enterprise Information Systems, 12-13 June, 2007, Funchal, Madeira - Portugal.
- The IBM Verification Conference 2006. October 23 - 26, 2006. Organized by IBM Research Lab in Haifa, Israel.
- ICSEA 2006, International Conference on Software Engineering Advances.
- SEFM 2006, 4th IEEE International Conference on Software Engineering and Formal Methods.
- FMICS 2006, 11th International Workshop on Formal Methods for Industrial Critical Systems.

- PADTAD 2006, Parallel and Distributed Systems: Testing and Debugging.
- MSVVEIS 2006, The Fourth International Workshop on Modelling, Simulation, Verification and Validation of Enterprise Information Systems.
- TACAS 2006, The 12th International Conference on Tools and Algorithms for the Construction and Analysis of Systems.
- RV 2005, The 5th International Workshop on Runtime Verification.
- VVEIS 2005, The 3rd International Workshop on Verification and Validation of Enterprise Information Systems.
- SEFM 2005, 3rd IEEE International Conference on Software Engineering and Formal Methods.
- FATES 2005, Formal Approaches to Testing of Software.
- ICI 2004, International Conference on Informatics.
- CAV 2004, The 16th Conference in Computer Aided Verification.
- DAW 2004, Dynamic Aspects Workshop.
- SFEDL 2004, Semantic Foundations of Engineering Design Languages.
- REOS 2003, Workshop on Requirements Engineering and Open Systems.
- ASARTI 2003, Workshop: Advancing the State-of-the-Art in Run-Time Inspection.
- FMPPTA 2003, The 8th International Workshop on Formal Methods for Parallel Programming: Theory and Applications.
- DSN'03, Workshop on Model Checking for Dependable Software-Intensive Systems.
- ESEC/FSE'03, 4th joint meeting of the European Software Engineering Conference and ACM SIGSOFT Symposium on the Foundations of Software Engineering.
- CAV 2002, The 14th Conference in Computer Aided Verification.
- RT-TOOLS, 2002 Workshop on Real-Time Tools.
- FMPPTA 2002, The 7th International Workshop on Formal Methods for Parallel Programming: Theory and Applications.
- SPIN 2002, The 9th International SPIN Workshop on Model Checking of Software.
- SPIN 2001, The 8th International SPIN Workshop on Model Checking of Software.
- FMPPTA 2001, 6th International Workshop on Formal Methods for Parallel Programming: Theory and Applications.
- MVI 2001, Model-based Validation of Intelligence.
- Lfm 2000, Fifth NASA Langley Formal Methods Workshop.
- PATV 2000, The First International Workshop on Automated Program Analysis, Testing and Verification.
- JFLA 2000, Les onzimes Journées Francophones des Langages Applicatifs.

7 Teaching

Most of my carrier has been spent in research labs, so my teaching experience is limited. I have given the following classes as appointed Lecturer in Computer Science at California Institute of Technology (<http://www.caltech.edu>):

- A class on *program monitoring* techniques during the period April 28 - May 29, 2008, as the second half of a class on *Reliable Software: Testing and Monitoring*. See course website: <http://www.runtime-verification.org/course>.
- A modified version of this course was given during May 2009. See course website: <http://www.runtime-verification.org/course09>.

References

Books and Book Chapters

- [1] The RAISE Language Group. *The RAISE Specification Language*. The BCS Practitioner Series. Prentice Hall, 1992. The book consists of two parts I: RSL Tutorial (pages 9-250), and II: RSL Reference Description (pages 251-369). Havelund wrote part I, while Havelund together with Anne Haxthausen wrote part II. The language presented in the book is designed by the *RAISE Language Group*: Chris George, Peter Haff, Klaus Havelund, Anne Haxthausen, Robert Milne, Claus Bendix Nielsen, Soeren Prehn and Kim Ritter Wagner.
- [2] Klaus Havelund. *RAISE in Perspective*. Invited chapter in *Logics of Specification Languages*. Edited by Dines Bjørner and Martin Henson. Monographs in Theoretical Computer Science. An EATCS Series, 2007, 624 p., Hardcover.

Journal Papers

- [3] Rahul Agarwal, Saddek Bensalem, Eitan Farchi, Klaus Havelund, Yarden Nir-Buchbinder, Scott D. Stoller, Shmuel Ur, and Liqiang Wang. Detection of Deadlock Potentials in Multi-Threaded Programs. *IBM Journal of Research & Development*, to appear. November 2009.
- [4] Cyrille Artho, Howard Barringer, Allen Goldberg, Klaus Havelund, Sarfraz Khurshid, Mike Lowry, Corina Pasareanu, Grigore Rosu, Koushik Sen, Willem Visser, and Rich Washington. Combining Test-Case Generation and Runtime Verification. *Journal of Theoretical Computer Science*, 336(2-3), May 2005. Extended version of [24].
- [5] Cyrille Artho, Klaus Havelund, and Armin Biere. High-Level Data Races. *Software Testing, Verification and Reliability*, 13(4), 2004. Extended version of [27]. To appear.
- [6] Howard Barringer, Alex Groce, Klaus Havelund and Margaret Smith. Formal Analysis of Log Files. *Journal of Aerospace Computing, Information, and Communication*, to appear 2010. Invited talk given at the *SMC-IT'09 workshop: Software Reliability for Space Missions*. July 20, 2009, Pasadena, California, USA.

- [7] Matthew Bennett, Richard Borgen, Klaus Havelund, Michel Ingham and David Wagner. Prototyping a Domain-Specific Language for Monitor and Control Systems. *Journal of Aerospace Computing, Information, and Communication*, to appear 2010. Journal version of [34].
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